



(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/816,103
Filing Date	March 31, 2004
First Named Inventor	Edward T. Grochowski
Art Unit	2183
Examiner Name	Unknown
Attorney Docket Number	42P18305

U.S. PATENT DOCUMENTS

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/816,103
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Sheet	X 2	of	2	Attorney Docket Number	42P18305

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
RF		TULLSEN et al - January 1999 - www.cs.washington.edu/research/smt/papers/hpca.ps> retrieved on 7/25/2005 - Supporting fine-grained synchronization on a simultaneous multithreading processor - SYMPOSIUM ON HIGH PERFORMANCE COMPUTER ARCHITECTURE Online!	

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RF		ALPERT, DON, "Will Microprocessors Become Simpler?", Multithreading May Reverse the Trend Toward Increasing Processor Complexity, Microprocessor Report, The Insider's Guide To Microprocessor Hardware, www.MPRonline.com, November 17, 2003, pp. 1-4.		
RF		GONTMAKHER, ALEX, et al., "Intrathreads: Techniques For Parallelizing Sequential Code", Abstract, gsash@cs.technion.ac.il, pp. 1-9.		
RF		INTEL, "Introduction To The IA-32 Intel Architecture, Chpt. 2, pp. 1-13.		
RF		KISSEL, KEVIN D., "Multithreading Extensions To The MIPS® Architecture", MIPS Technologies, At the core of the user experience.™, Copyright © 2003 MIPS Technologies, Inc. All rights reserved.pp. 1-23		
RF		SUN MICROSYSTEMS, "MAJCTM Architecture Tutorial", Palo Alto, CA 94303, USA, Internet: www.son.com/microelectronics, pp. 31		
RF		LEVY, MARKUS, "Multithread Technologies Disclosed At MPF", Imagination and MIPS Discuss Details of New Architectures, Microprocessor Report, The Insider's Guide To Microprocessor Hardware, www.MPRonline.com, November 10, 2003, pp. 1-4.		
RF		SIGNORELLI, ROBERT D., "MAJC: Innovation In Parallelism", pp. 1-6.		

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RF		SOHI, GURINDAR S., et al., "Speculative Multithreaded Processors", Cover Feature, University of Wisconsin-Madison, pp. 66-72.	
RF		SRIRAM, PARTHASARATHY, et al., "MPEG-2 Video Decompression On A Multi-Processing VLIW Microprocessor", Sun Microsystems, Inc., 901 San Antonio Road, Palo Alto, CA 94303, USA, Presented at ICCE, June 2000, Los Angeles, CA, USA, © IEEE 2000, pp. 2.	
RF		SUDHARSANAN, SUDHARSANAN, "MAJC-5200: A High Performance Microprocessor For Multimedia Computing", Sun Microsystems, Inc., Palo Alto, CA 94303, USA, Abstract, pp. 8.	
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RF		TREMBLAY, MARC, "The MAJC Architecture: A Synthesis Of Parallelism And Scalability", Sun Microsystems, 901 San Antonio Rd., MS USUN03-202, Palo Alto, CA 94303, USA, marc.tremblay@sun.com, November - December 2000, pp. 25	

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